

1 / 4

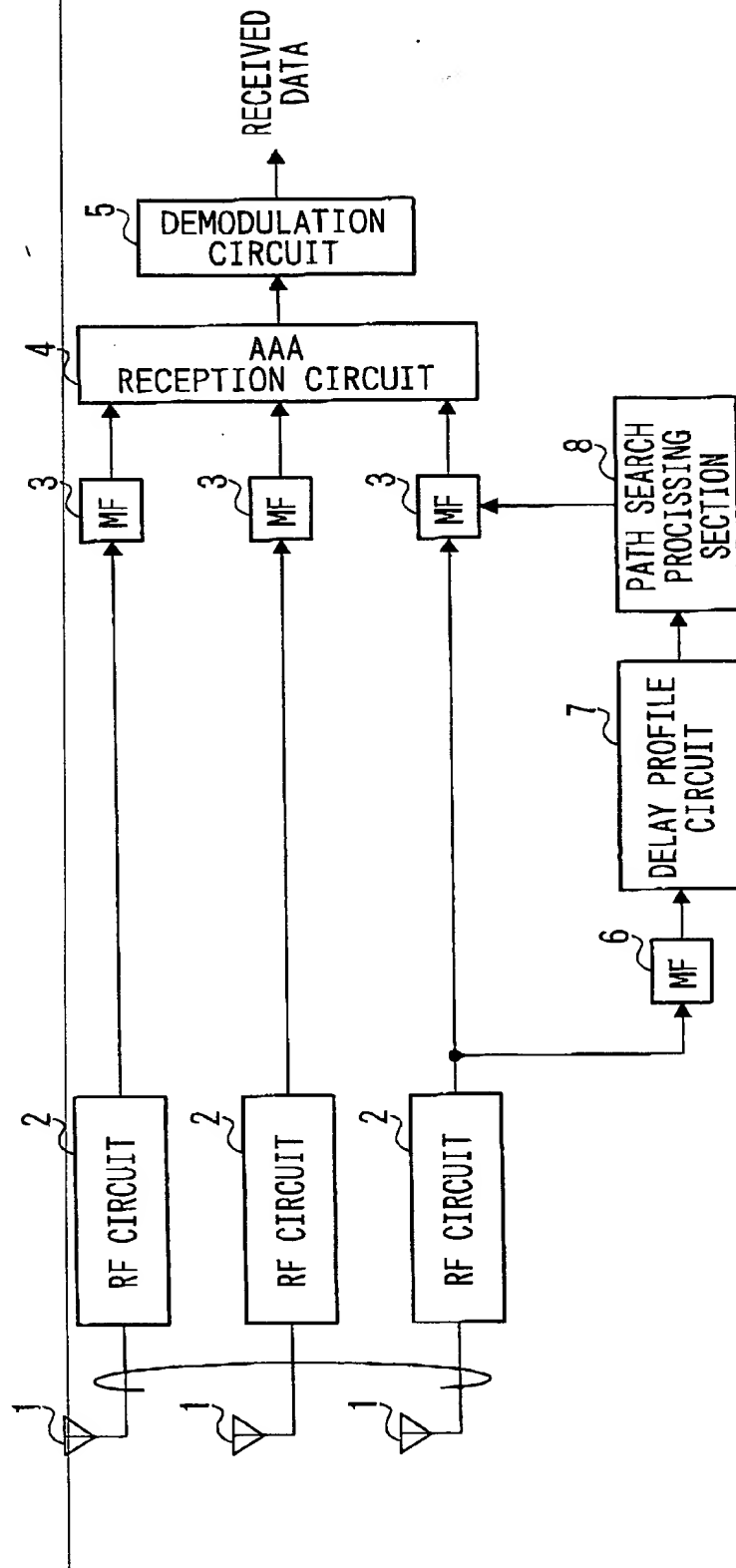


FIG. 1

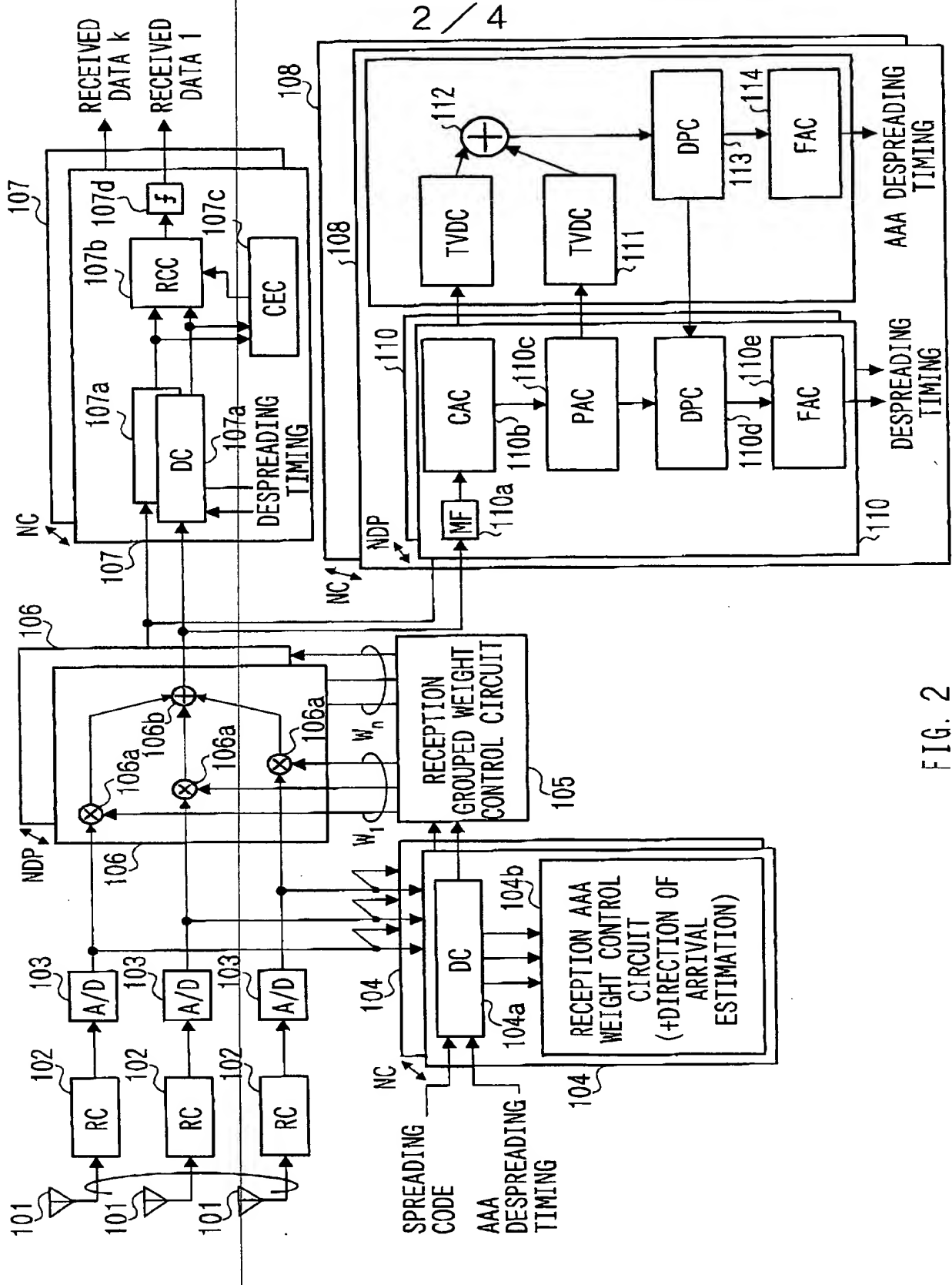


FIG. 2

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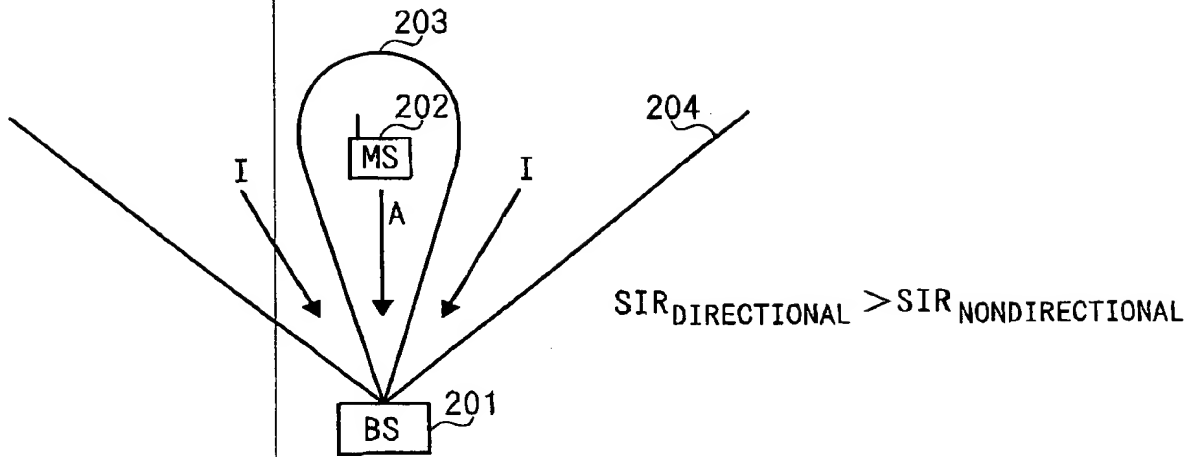


FIG. 3

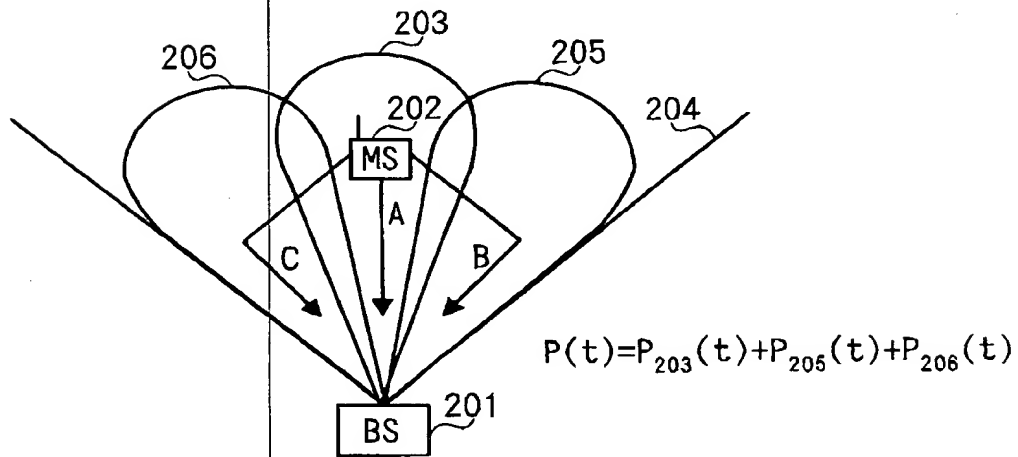


FIG. 4

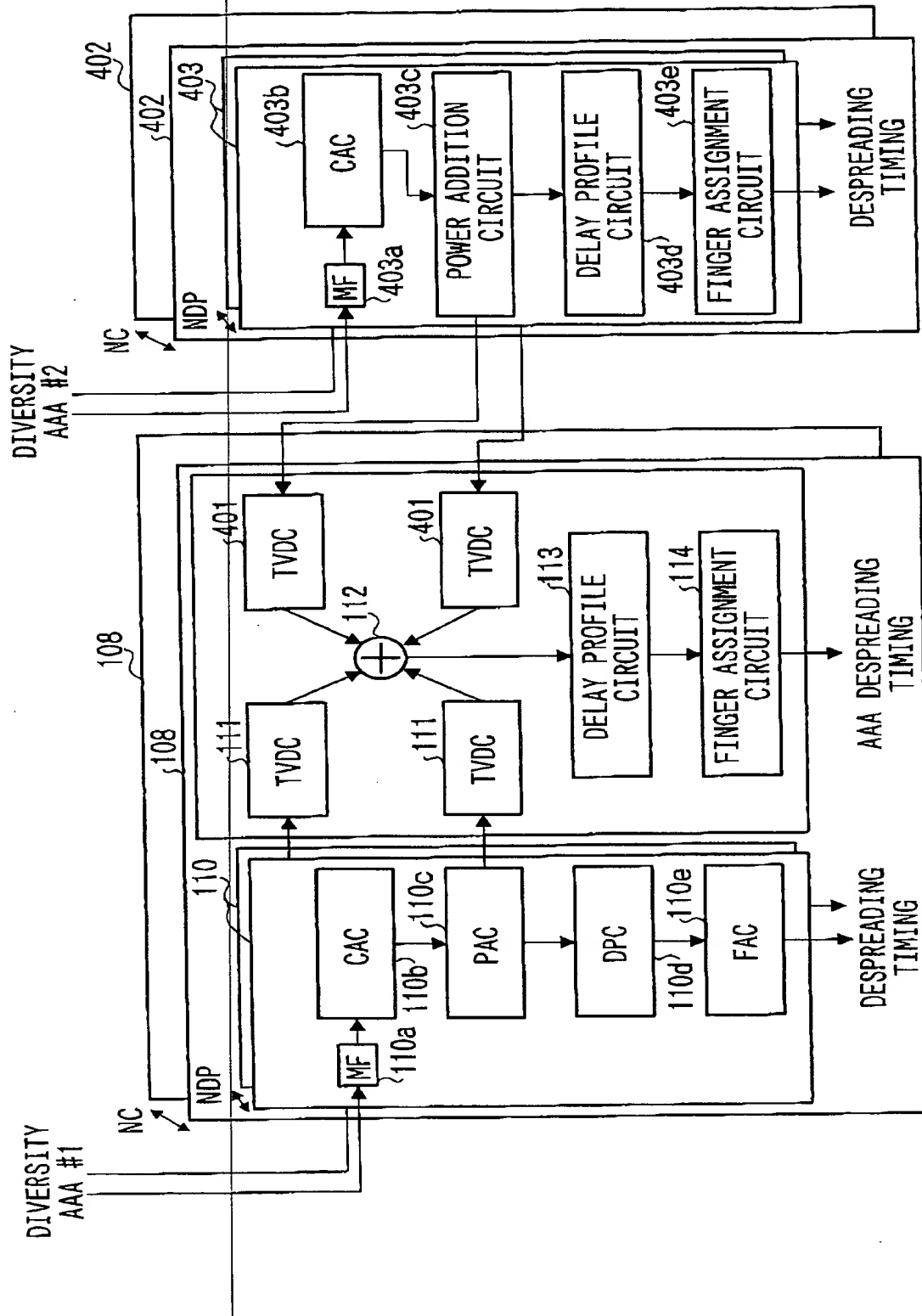


FIG. 5

FIG.2

102 RC : RF CIRCUIT
106,110 NDP : NUMBER OF DIRECTIONAL PATTERNS
107,108 NC : NUMBER OF CHANNELS
104a,107a DC : DESPREADING CIRCUIT
107b RCC : RAKE COMBINING CIRCUIT
107c CEC : CHANNEL ESTIMATION CIRCUIT
110b CAC : COHERENT ADDITION CIRCUIT
110c PAC : POWER ADDITION CIRCUIT
110d,113 DPC : DELAY PROFILE CIRCUIT
110e,114 FAC : FINGER ASSIGNMENT CIRCUIT
111 TVDC : THRESHOLD VALUE DECISION CIRCUIT

FIG.5

108,402 NC : NUMBER OF CHANNELS
110,403 NDP : NUMBER OF DIRECTIONAL PATTERNS
110b,403b CAC : COHERENT ADDITION CIRCUIT
110c PAC : POWER ADDITION CIRCUIT
110d,113 DPC : DELAY PROFILE CIRCUIT
110e,114 FAC : FINGER ASSIGNMENT CIRCUIT
111,401 TVDC : THRESHOLD VALUE DECISION CIRCUIT